

In the Claims

Amend the Claims as follows:

1-20. (Canceled)

21. (New) A voltage supply circuit, comprising:

a first JFET transistor providing for a majority of an input-to-output voltage drop across its drain-source which is in series connection with a power supply input; and

a second JFET transistor providing for a minority of said input-to-output voltage drop, and having its drain-source connected in series with the drain-source of the first JFET transistor, and where both are in series with a load output;

wherein, the gate current of the first JFET transistor is connected to flow through said load output and not directly to ground; and

wherein, the gate of the second JFET transistor is connected to ground.

22. (New) A method for improving the efficiency of a voltage supply circuit, comprising:

dropping a majority of an input-to-output voltage drop across the drain-source of a first JFET transistor which is in series connection with a power supply input; and

dropping a minority of said input-to-output voltage drop across the drain-source of a second JFET transistor, having its drain-source connected in series with the drain-source of said first JFET, and where both are in series with a load output;

steering the gate current of the first JFET transistor to flow through said load output and not directly to ground; and

connecting the gate of the second JFET transistor to ground.